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### Contact Details

- DKTEs Yashwantrao Chavan Polytechnic ,R. S. No-644, Sangali Road, near Adinath Housing Society, Ichankaranji-4161

### Qualification

- Diploma in Industrial Electronics (DIE)
- Bachelor of Electronics and Telecommunication(BE)
- Master of Electronics (ME ETRX) VLSI and Embedded System

### Area of Expertise

- VLSI and Embedded System
- Mechatronics
- Microcontroller.
- Consumer Electronics
- Basic Power Electronics
- Basic Electronics

### Workshop / Training

- Completed the Training in 'Satellite Telecommunication' Conducted by M/S. Institute of Satellite Training Center,Pune
- Completed the Training in 'Power Plant Familiarization' Conducted by Reliance Infra.Ltd 2x50 MW Dahanu Thermal Power Station, Dahanu.
- Completed the Training in 'Basic Course On Automation' Conducted by Siemens Ltd Mumbai.
- Completed the 'Induction Training Programme Phase-1' Conducted by The NITTTR Bhopal
- Completed the Training in 'Advances in Engineering Materials & Testing Techniques' Conducted by Government Polytechnic, Pune.
- Completed the Orientation Program for Implementing 'I' Scheme Curriculum Conducted by MSBTE Mumbai.
- Completed the Training 'Embedded System design and automation' Conducted by M/s National Institute of Electronics and Information Technology(NIELITE), Aurangabad
- Completed the Workshop on 'Outcome Based Paper Setting' Conducted by the Examination Department ,Government Polytechnic ,Kolhapur

### Achievements

- Three International Paper Published
- Two National Paper Published
- One Journal

### Work Experience

- Six years working as Lecturer in Electronics and Telecommunication Engineering Department as SCJIT Polytechnic at Narsobawadi.
- Three years working as a Training and Placement Officer as SCJIT Polytechnic Narsobawadi.
- Three years working as Sealing Supervisor and Officer In charge in SCJIT Polytechnic.
- Three years working as Project coordinator in ENTC Department.
- Guided the 16 Diploma Student on different Project in different Stream.
- Two year working as External Vigilance Squad Member of MSCIT EXAM.
- Two year working as Controller of MSCIT EXAM.
- Two year working as Invigilator of GATE EXAM.
- One year working as Co-ordinator for the Facilitation Center.

### Industrial Projects

<b>Area</b>	<b>B.E(Electronics and Communication)</b>	<b>Duration:</b> 12 months
<b>Project title</b>	<b>Digital Power Supply using Keypad.</b>	
<b>Objective</b>	Input supply voltage & Current press key on keypad. Instead of knob using keypad giving input DC supply. By using Microcontroller 8085 for embedded design. In this project we use Graphic display. It shows V/I graph on display.	

<b>Area</b>	<b>Engineering Diploma (Industrial Electronics)</b>	<b>Duration:</b> 12 months
<b>Project title</b>	<b>Biometric Sensor Based Locker System.</b>	
<b>Objective</b>	When any user wants to open the lock he must place his finger. If he presses other key then message on key on LCD that "Place finger". So that he must press that open key. After pressing open key there is message "Enter your code". Now user should enter a valid code and place his finger. If placed finger matches with the already enrolled finger, then there is signal from microcontroller to solenoid drive circuit.	

<b>Area</b>	<b>M.E(Electronics)</b>	<b>Duration:</b> 12 months
<b>Project title</b>	<b>Implementation of Image Scaling algorithm on FPGA.</b>	
<b>Objective</b>	The aim is to implement the image scaling processor at a very low cost. The image scaling process is done for 2D image and results are analyzed by their PSNR.To obtain the qualities of the scaled images by Bilinear scaling algorithm.	